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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Romain DESPLATS et al.

Serial No. 09/831,525

GROUP 2184

Filed May 10, 2001

Examiner Unknown

METHOD AND INSTALLATION FOR FAST LOCATION OF A FAULT IN AN

INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were discovered during any searches they or their client had made, or that they were considered in the preparation of the application.

A concise description of the relevance of the French language item is given on page 4 of the present specification, where its title if translated into English.

Technology Center 2100

JUL 31 2001

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Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that no item contained in this Information Disclosure Statement was known to any individual designated in §1.56(c), more than three months prior to the filing of this Statement.

Respectfully submitted,

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By



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July 27, 2001